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# Mandating the Standard Library: Clause 31 - Atomics library

With the adoption of P0788R3, we have a new way of specifying requirements for the library clauses of the standard. This is one of a series of papers reformulating the requirements into the new format. This effort was strongly influenced by the informational paper P1369R0.

The changes in this series of papers fall into three broad categories.

- Change "participate in overload resolution" wording into "Constraints" elements
- Change "Requires" elements into either "Mandates" or "Expects", depending (mostly) on whether or not they can be checked at compile time.
- Drive-by fixes (hopefully very few)

This paper covers Clause 31 (Atomics), and is based on N4830.

The entire clause is reproduced here, but the changes are confined to a few sections:

- atomics.ref.generic 31.7
- atomics.ref.operations 31.7.1
- atomics.ref.pointer 31.7.4
- atomics.types.operations 31.8.1
- atomics.types.pointer 31.8.4
- atomics.nonmembers 31.9
- atomics.flag 31.10

#### Drive-by fixes:

- I strengthened the wording in [atomics.ref.generic] 31.7 to require a compile-time failure for non-trivially copyable Ts.
- I removed a "shall" from the definition of ATOMIC\_INIT in [atomics.flag] 31.10.

#### Open questions:

— Should we change all the memory\_order\_seq\_cst to memory\_order::seq\_cst (and others)? - this will be done via a pull-request post-Belfast. See https://github.com/cplusplus/draft/issues/3365

#### Changes from R0:

- Incorporated changes from P1505, and changed several "is not" into "is neither".
- Updated to N4830.

#### Changes from R1:

— Items from the LWG review at 11-Oct teleconference.

Thanks to Daniel Krügler for his advice and reviews.

Thanks to Daniel Sunderland for his paper P1505, which suggested similar changes.

Help for the editors: The changes here can be viewed as latex sources with the following commands

git clone git@github.com:mclow/mandate.git
cd mandate
git diff master..chapter31 atomics.tex

## 31 Atomic operations library

## [atomics]

31.1 General [atomics.general]

1 This Clause describes components for fine-grained atomic access. This access is provided via operations on atomic objects.

<sup>2</sup> The following subclauses describe atomics requirements and components for types and operations, as summarized in Table 142.

Table 142: Atomics library summary [tab:atomics.summary]

	Subclause	Header
31.3	Type aliases	<atomic></atomic>
31.4	Order and consistency	
31.5	Lock-free property	
31.6	Waiting and notifying	
31.7	Class template atomic_ref	
31.8	Class template atomic	
31.9	Non-member functions	
31.10	Flag type and operations	
31.11	Fences	

#### 31.2 Header <atomic> synopsis

[atomics.syn]

```
namespace std {
  // 31.4, order and consistency
  enum class memory_order : unspecified;
  template<class T>
    T kill_dependency(T y) noexcept;
  // 31.5, lock-free property
  #define ATOMIC_BOOL_LOCK_FREE unspecified
  #define ATOMIC_CHAR_LOCK_FREE unspecified
  #define ATOMIC_CHAR8_T_LOCK_FREE unspecified
  #define ATOMIC_CHAR16_T_LOCK_FREE unspecified
  \verb|#define ATOMIC_CHAR32_T_LOCK_FREE unspecified|\\
  \verb|#define ATOMIC_WCHAR_T_LOCK_FREE unspecified|\\
  #define ATOMIC_SHORT_LOCK_FREE unspecified
  #define ATOMIC_INT_LOCK_FREE unspecified
  \verb|#define ATOMIC_LONG_LOCK_FREE| unspecified|
  \verb|#define ATOMIC_LLONG_LOCK_FREE| unspecified|
  #define ATOMIC_POINTER_LOCK_FREE unspecified
  // 31.7, class template atomic_ref
  template<class T> struct atomic_ref;
  // 31.7.4, partial specialization for pointers
  template<class T> struct atomic_ref<T*>;
  // 31.8, class template atomic
  template<class T> struct atomic;
  // 31.8.4, partial specialization for pointers
  template<class T> struct atomic<T*>;
  // 31.8.1, initialization
  #define ATOMIC_VAR_INIT(value) see below
```

```
// 31.9, non-member functions
template<class T>
 bool atomic_is_lock_free(const volatile atomic<T>*) noexcept;
template<class T>
 bool atomic_is_lock_free(const atomic<T>*) noexcept;
template<class T>
 void atomic_init(volatile atomic<T>*, typename atomic<T>::value_type) noexcept;
template<class T>
 void atomic_init(atomic<T>*, typename atomic<T>::value_type) noexcept;
template<class T>
 void atomic_store(volatile atomic<T>*, typename atomic<T>::value_type) noexcept;
template<class T>
 void atomic_store(atomic<T>*, typename atomic<T>::value_type) noexcept;
template<class T>
 void atomic_store_explicit(volatile atomic<T>*, typename atomic<T>::value_type,
                             memory_order) noexcept;
template<class T>
 void atomic_store_explicit(atomic<T>*, typename atomic<T>::value_type,
                             memory_order) noexcept;
template<class T>
 T atomic_load(const volatile atomic<T>*) noexcept;
template<class T>
 T atomic_load(const atomic<T>*) noexcept;
template<class T>
 T atomic_load_explicit(const volatile atomic<T>*, memory_order) noexcept;
template<class T>
 T atomic_load_explicit(const atomic<T>*, memory_order) noexcept;
template<class T>
 T atomic_exchange(volatile atomic<T>*, typename atomic<T>::value_type) noexcept;
template<class T>
 T atomic_exchange(atomic<T>*, typename atomic<T>::value_type) noexcept;
template<class T>
 T atomic_exchange_explicit(volatile atomic<T>*, typename atomic<T>::value_type,
                             memory_order) noexcept;
template<class T>
 T atomic_exchange_explicit(atomic<T>*, typename atomic<T>::value_type,
                             memory_order) noexcept;
template<class T>
 bool atomic_compare_exchange_weak(volatile atomic<T>*,
                                    typename atomic<T>::value_type*,
                                    typename atomic<T>::value_type) noexcept;
template<class T>
 bool atomic_compare_exchange_weak(atomic<T>*,
                                    typename atomic<T>::value_type*,
                                    typename atomic<T>::value_type) noexcept;
template<class T>
 bool atomic_compare_exchange_strong(volatile atomic<T>*,
                                      typename atomic<T>::value_type*,
                                      typename atomic<T>::value_type) noexcept;
template < class T>
 bool atomic_compare_exchange_strong(atomic<T>*,
                                      typename atomic<T>::value_type*,
                                      typename atomic<T>::value_type) noexcept;
template<class T>
 bool atomic_compare_exchange_weak_explicit(volatile atomic<T>*,
                                             typename atomic<T>::value_type*,
                                             typename atomic<T>::value_type,
                                             memory_order, memory_order) noexcept;
template<class T>
 bool atomic_compare_exchange_weak_explicit(atomic<T>*,
                                             typename atomic<T>::value_type*,
                                             typename atomic<T>::value_type,
                                             memory_order, memory_order) noexcept;
```

```
template<class T>
 bool atomic_compare_exchange_strong_explicit(volatile atomic<T>*,
                                               typename atomic<T>::value_type*,
                                               typename atomic<T>::value_type,
                                               memory_order, memory_order) noexcept;
template<class T>
 bool atomic_compare_exchange_strong_explicit(atomic<T>*,
                                               typename atomic<T>::value_type*,
                                               typename atomic<T>::value_type,
                                               memory_order, memory_order) noexcept;
template<class T>
 T atomic_fetch_add(volatile atomic<T>*, typename atomic<T>::difference_type) noexcept;
template<class T>
 T atomic_fetch_add(atomic<T>*, typename atomic<T>::difference_type) noexcept;
template<class T>
 T atomic_fetch_add_explicit(volatile atomic<T>*, typename atomic<T>::difference_type,
                              memory_order) noexcept;
template<class T>
 T atomic_fetch_add_explicit(atomic<T>*, typename atomic<T>::difference_type,
                              memory_order) noexcept;
template<class T>
 T atomic_fetch_sub(volatile atomic<T>*, typename atomic<T>::difference_type) noexcept;
template<class T>
 T atomic_fetch_sub(atomic<T>*, typename atomic<T>::difference_type) noexcept;
template<class T>
 T atomic_fetch_sub_explicit(volatile atomic<T>*, typename atomic<T>::difference_type,
                              memory_order) noexcept;
template<class T>
 T atomic_fetch_sub_explicit(atomic<T>*, typename atomic<T>::difference_type,
                              memory_order) noexcept;
template<class T>
 T atomic_fetch_and(volatile atomic<T>*, typename atomic<T>::value_type) noexcept;
template<class T>
 T atomic_fetch_and(atomic<T>*, typename atomic<T>::value_type) noexcept;
template<class T>
 T atomic_fetch_and_explicit(volatile atomic<T>*, typename atomic<T>::value_type,
                              memory_order) noexcept;
template<class T>
 T atomic_fetch_and_explicit(atomic<T>*, typename atomic<T>::value_type,
                              memory_order) noexcept;
template<class T>
 T atomic_fetch_or(volatile atomic<T>*, typename atomic<T>::value_type) noexcept;
template<class T>
 T atomic_fetch_or(atomic<T>*, typename atomic<T>::value_type) noexcept;
template<class T>
 T atomic_fetch_or_explicit(volatile atomic<T>*, typename atomic<T>::value_type,
                             memory_order) noexcept;
template<class T>
 T atomic_fetch_or_explicit(atomic<T>*, typename atomic<T>::value_type,
                             memory_order) noexcept;
template<class T>
 T atomic_fetch_xor(volatile atomic<T>*, typename atomic<T>::value_type) noexcept;
template<class T>
 T atomic_fetch_xor(atomic<T>*, typename atomic<T>::value_type) noexcept;
template<class T>
 T atomic_fetch_xor_explicit(volatile atomic<T>*, typename atomic<T>::value_type,
                              memory_order) noexcept;
template<class T>
 T atomic_fetch_xor_explicit(atomic<T>*, typename atomic<T>::value_type,
                              memory_order) noexcept;
template<class T>
 void atomic_wait(const volatile atomic<T>*, typename atomic<T>::value_type);
```

```
template<class T>
  void atomic_wait(const atomic<T>*, typename atomic<T>::value_type);
template<class T>
  void atomic_wait_explicit(const volatile atomic<T>*, typename atomic<T>::value_type,
                             memory_order);
template<class T>
  void atomic_wait_explicit(const atomic<T>*, typename atomic<T>::value_type,
                             memory_order);
template<class T>
  void atomic_notify_one(volatile atomic<T>*);
template<class T>
  void atomic_notify_one(atomic<T>*);
template<class T>
  void atomic_notify_all(volatile atomic<T>*);
template<class T>
  void atomic_notify_all(atomic<T>*);
// 31.3, type aliases
                         = atomic<bool>;
= atomic<char>;
using atomic_bool
using atomic_char
                        = atomic<cnai>,
= atomic<signed char>;
= atomic<unsigned char>;
= atomic<short>;
= atomic<unsigned short>;
= atomic<int>;
using atomic_schar
using atomic_uchar
using atomic_short
using atomic_ushort
using atomic_int
using atomic_wchar_t
                           = atomic<wchar_t>;
using atomic_int8_t
                            = atomic<int8_t>;
using atomic_uint8_t using atomic_int16_t
                            = atomic<uint8_t>;
                            = atomic<int16_t>;
using atomic_uint16_t
                            = atomic<uint16_t>;
using atomic_int32_t
                            = atomic<int32_t>;
using atomic_uint32_t
                            = atomic<uint32_t>;
using atomic_int64_t
                            = atomic<int64_t>;
using atomic_uint64_t
                            = atomic<uint64_t>;
using atomic_int_least8_t = atomic<int_least8_t>;
using atomic_uint_least8_t = atomic<uint_least8_t>;
using atomic_int_least16_t = atomic<int_least16_t>;
using atomic_uint_least16_t = atomic<uint_least16_t>;
using atomic_int_least32_t = atomic<int_least32_t>;
using atomic_uint_least32_t = atomic<uint_least32_t>;
using atomic_int_least64_t = atomic<int_least64_t>;
using atomic_uint_least64_t = atomic<uint_least64_t>;
using atomic_int_fast8_t = atomic<int_fast8_t>;
using atomic_uint_fast8_t = atomic<uint_fast8_t>;
using atomic_int_fast16_t = atomic<int_fast16_t>;
using atomic_uint_fast16_t = atomic<uint_fast16_t>;
using atomic_int_fast32_t = atomic<int_fast32_t>;
using atomic_uint_fast32_t = atomic<uint_fast32_t>;
using atomic_int_fast64_t = atomic<int_fast64_t>;
using atomic_uint_fast64_t = atomic<uint_fast64_t>;
                        = atomic<intptr_t>;
= atomic
using atomic_intptr_t
using atomic_uintptr_t
                            = atomic<uintptr_t>;
```

```
using atomic_size_t
                            = atomic<size_t>;
using atomic_ptrdiff_t
                           = atomic<ptrdiff_t>;
using atomic_intmax_t
                            = atomic<intmax_t>;
using atomic_uintmax_t
                           = atomic<uintmax_t>;
using atomic_signed_lock_free = see below;
using atomic_unsigned_lock_free = see below;
// 31.10, flag type and operations
struct atomic_flag;
#define ATOMIC_FLAG_INIT see below
bool atomic_flag_test(const volatile atomic_flag*) noexcept;
bool atomic_flag_test(const atomic_flag*) noexcept;
bool atomic_flag_test_explicit(const volatile atomic_flag*, memory_order) noexcept;
bool atomic_flag_test_explicit(const atomic_flag*, memory_order) noexcept;
bool atomic_flag_test_and_set(volatile atomic_flag*) noexcept;
bool atomic_flag_test_and_set(atomic_flag*) noexcept;
bool atomic_flag_test_and_set_explicit(volatile atomic_flag*, memory_order) noexcept;
bool atomic_flag_test_and_set_explicit(atomic_flag*, memory_order) noexcept;
void atomic_flag_clear(volatile atomic_flag*) noexcept;
void atomic_flag_clear(atomic_flag*) noexcept;
void atomic_flag_clear_explicit(volatile atomic_flag*, memory_order) noexcept;
void atomic_flag_clear_explicit(atomic_flag*, memory_order) noexcept;
void atomic_flag_wait(const volatile atomic_flag*, bool) noexcept;
void atomic_flag_wait(const atomic_flag*, bool) noexcept;
void atomic_flag_wait_explicit(const volatile atomic_flag*,
                               bool, memory_order) noexcept;
void atomic_flag_wait_explicit(const atomic_flag*,
                               bool, memory_order) noexcept;
void atomic_flag_notify_one(volatile atomic_flag*) noexcept;
void atomic_flag_notify_one(atomic_flag*) noexcept;
void atomic_flag_notify_all(volatile atomic_flag*) noexcept;
void atomic_flag_notify_all(atomic_flag*) noexcept;
// 31.11, fences
extern "C" void atomic_thread_fence(memory_order) noexcept;
extern "C" void atomic_signal_fence(memory_order) noexcept;
```

#### 31.3 Type aliases

[atomics.alias]

- <sup>1</sup> The type aliases atomic\_intN\_t, atomic\_uintN\_t, atomic\_intptr\_t, and atomic\_uintptr\_t are defined if and only if intN\_t, uintN\_t, intptr\_t, and uintptr\_t are defined, respectively.
- <sup>2</sup> The type aliases atomic\_signed\_lock\_free and atomic\_unsigned\_lock\_free name specializations of atomic whose template arguments are integral types, respectively signed and unsigned, and whose is\_-always\_lock\_free property is true. [Note: These aliases are optional in freestanding implementations (??).—end note] Implementations should choose for these aliases the integral specializations of atomic for which the atomic waiting and notifying operations (31.6) are most efficient.

#### 31.4 Order and consistency

[atomics.order]

```
namespace std {
  enum class memory_order : unspecified {
    relaxed, consume, acquire, release, acq_rel, seq_cst
  };
  inline constexpr memory_order memory_order_relaxed = memory_order::relaxed;
  inline constexpr memory_order memory_order_consume = memory_order::consume;
  inline constexpr memory_order memory_order_acquire = memory_order::acquire;
  inline constexpr memory_order memory_order_release = memory_order::release;
  inline constexpr memory_order memory_order_acq_rel = memory_order::acq_rel;
```

```
inline constexpr memory_order memory_order_seq_cst = memory_order::seq_cst;
}
```

<sup>1</sup> The enumeration memory\_order specifies the detailed regular (non-atomic) memory synchronization order as defined in ?? and may provide for operation ordering. Its enumerated values and their meanings are as follows:

- (1.1) memory\_order::relaxed: no operation orders memory.
- (1.2) memory\_order::release, memory\_order::acq\_rel, and memory\_order::seq\_cst: a store operation performs a release operation on the affected memory location.
- (1.3) memory\_order::consume: a load operation performs a consume operation on the affected memory location. [Note: Prefer memory\_order::acquire, which provides stronger guarantees than memory\_order::consume. Implementations have found it infeasible to provide performance better than that of memory\_order::acquire. Specification revisions are under consideration. end note]
- memory\_order::acquire, memory\_order::acq\_rel, and memory\_order::seq\_cst: a load operation performs an acquire operation on the affected memory location.

[Note: Atomic operations specifying memory\_order::relaxed are relaxed with respect to memory ordering. Implementations must still guarantee that any given atomic access to a particular atomic object be indivisible with respect to all other atomic accesses to that object. — $end\ note$ ]

- <sup>2</sup> An atomic operation A that performs a release operation on an atomic object M synchronizes with an atomic operation B that performs an acquire operation on M and takes its value from any side effect in the release sequence headed by A.
- <sup>3</sup> An atomic operation A on some atomic object M is coherence-ordered before another atomic operation B on M if
- (3.1) A is a modification, and B reads the value stored by A, or
- (3.2) A precedes B in the modification order of M, or
- (3.3) A and B are not the same atomic read-modify-write operation, and there exists an atomic modification X of M such that A reads the value stored by X and X precedes B in the modification order of M, or
- $^{(3.4)}$  there exists an atomic modification X of M such that A is coherence-ordered before X and X is coherence-ordered before B.
  - <sup>4</sup> There is a single total order S on all memory\_order::seq\_cst operations, including fences, that satisfies the following constraints. First, if A and B are memory\_order::seq\_cst operations and A strongly happens before B, then A precedes B in S. Second, for every pair of atomic operations A and B on an object M, where A is coherence-ordered before B, the following four conditions are required to be satisfied by S:
- (4.1) if A and B are both memory\_order::seq\_cst operations, then A precedes B in S; and
- (4.2) if A is a memory\_order::seq\_cst operation and B happens before a memory\_order::seq\_cst fence Y, then A precedes Y in S; and
- (4.3) if a memory\_order::seq\_cst fence X happens before A and B is a memory\_order::seq\_cst operation, then X precedes B in S; and
- (4.4) if a memory\_order::seq\_cst fence X happens before A and B happens before a memory\_order::seq\_-cst fence Y, then X precedes Y in S.
  - <sup>5</sup> [Note: This definition ensures that S is consistent with the modification order of any atomic object M. It also ensures that a memory\_order::seq\_cst load A of M gets its value either from the last modification of M that precedes A in S or from some non-memory\_order::seq\_cst modification of M that does not happen before any modification of M that precedes A in S. —end note]
  - <sup>6</sup> [Note: We do not require that S be consistent with "happens before" (??). This allows more efficient implementation of memory\_order::acquire and memory\_order::release on some machine architectures. It can produce surprising results when these are mixed with memory\_order::seq\_cst accesses. end note]
  - <sup>7</sup> [Note: memory\_order::seq\_cst ensures sequential consistency only for a program that is free of data races and uses exclusively memory\_order::seq\_cst atomic operations. Any use of weaker ordering will invalidate this guarantee unless extreme care is used. In many cases, memory\_order::seq\_cst atomic operations are reorderable with respect to other atomic operations performed by the same thread. —end note]

8 Implementations should ensure that no "out-of-thin-air" values are computed that circularly depend on their own computation.

```
[Note: For example, with x and y initially zero,
  // Thread 1:
  r1 = y.load(memory_order::relaxed);
  x.store(r1, memory_order::relaxed);
  // Thread 2:
  r2 = x.load(memory_order::relaxed);
  y.store(r2, memory_order::relaxed);
```

should not produce r1 == r2 == 42, since the store of 42 to y is only possible if the store to x stores 42, which circularly depends on the store to y storing 42. Note that without this restriction, such an execution is possible. — end note]

<sup>9</sup> [Note: The recommendation similarly disallows r1 == r2 == 42 in the following example, with x and y again initially zero:

```
// Thread 1:
r1 = x.load(memory_order::relaxed);
if (r1 == 42) y.store(42, memory_order::relaxed);
// Thread 2:
r2 = y.load(memory_order::relaxed);
if (r2 == 42) x.store(42, memory_order::relaxed);
-- end note
```

- Atomic read-modify-write operations shall always read the last value (in the modification order) written before the write associated with the read-modify-write operation.
- <sup>11</sup> Implementations should make atomic stores visible to atomic loads within a reasonable amount of time.

```
template<class T>
   T kill_dependency(T y) noexcept;
```

- 12 Effects: The argument does not carry a dependency to the return value (??).
- 13 Returns: y.

#### 31.5 Lock-free property

[atomics.lockfree]

```
#define ATOMIC_BOOL_LOCK_FREE unspecified
#define ATOMIC_CHAR_LOCK_FREE unspecified
#define ATOMIC_CHAR8_T_LOCK_FREE unspecified
#define ATOMIC_CHAR16_T_LOCK_FREE unspecified
#define ATOMIC_CHAR32_T_LOCK_FREE unspecified
#define ATOMIC_WCHAR_T_LOCK_FREE unspecified
#define ATOMIC_SHORT_LOCK_FREE unspecified
#define ATOMIC_INT_LOCK_FREE unspecified
#define ATOMIC_LONG_LOCK_FREE unspecified
#define ATOMIC_LONG_LOCK_FREE unspecified
#define ATOMIC_LONG_LOCK_FREE unspecified
#define ATOMIC_LONG_LOCK_FREE unspecified
#define ATOMIC_POINTER_LOCK_FREE unspecified
```

- <sup>1</sup> The ATOMIC\_...\_LOCK\_FREE macros indicate the lock-free property of the corresponding atomic types, with the signed and unsigned variants grouped together. The properties also apply to the corresponding (partial) specializations of the atomic template. A value of 0 indicates that the types are never lock-free. A value of 1 indicates that the types are always lock-free.
- <sup>2</sup> At least one signed integral specialization of the atomic template, along with the specialization for the corresponding unsigned type (??), is always lock-free. [Note: This requirement is optional in freestanding implementations (??). end note]
- <sup>3</sup> The function atomic\_is\_lock\_free (31.8.1) indicates whether the object is lock-free. In any given program execution, the result of the lock-free query shall be consistent for all pointers of the same type.
- <sup>4</sup> Atomic operations that are not lock-free are considered to potentially block (??).
- <sup>5</sup> [Note: Operations that are lock-free should also be address-free. That is, atomic operations on the same memory location via two different addresses will communicate atomically. The implementation should not

depend on any per-process state. This restriction enables communication by memory that is mapped into a process more than once and by memory that is shared between two processes.  $-end \ note$ 

#### 31.6 Waiting and notifying

[atomics.wait]

- Atomic waiting operations and atomic notifying operations provide a mechanism to wait for the value of an atomic object to change more efficiently than can be achieved with polling. An atomic waiting operation may block until it is unblocked by an atomic notifying operation, according to each function's effects. [Note: Programs are not guaranteed to observe transient atomic values, an issue known as the A-B-A problem, resulting in continued blocking if a condition is only temporarily met. end note]
- <sup>2</sup> [Note: The following functions are atomic waiting operations:

```
(2.1)
       — atomic<T>::wait,
(2.2)
       — atomic_flag::wait,
(2.3)
       — atomic_wait and atomic_wait_explicit,
(2.4)
       — atomic_flag_wait and atomic_flag_wait_explicit, and
(2.5)
       — atomic_ref<T>::wait.
     -end note
  3
    [Note: The following functions are atomic notifying operations:
(3.1)
      — atomic<T>::notify_one and atomic<T>::notify_all,
       — atomic_flag::notify_one and atomic_flag::notify_all,
(3.3)
       — atomic_notify_one and atomic_notify_all,
(3.4)

    atomic_flag_notify_one and atomic_flag_notify_all, and

(3.5)
       — atomic_ref<T>::notify_one and atomic_ref<T>::notify_all.
```

- <sup>4</sup> A call to an atomic waiting operation on an atomic object M is *eligible to be unblocked* by a call to an atomic notifying operation on M if there exist side effects X and Y on M such that:
- (4.1) the atomic waiting operation has blocked after observing the result of X,
- (4.2) X precedes Y in the modification order of M, and
- (4.3) Y happens before the call to the atomic notifying operation.

### 31.7 Class template atomic\_ref

- end note]

[atomics.ref.generic]

```
namespace std {
  template<class T> struct atomic_ref {
 private:
                        // exposition only
    T* ptr;
  public:
    using value_type = T;
    static constexpr bool is_always_lock_free = implementation-defined;
    static constexpr size_t required_alignment = implementation-defined;
    atomic_ref& operator=(const atomic_ref&) = delete;
    explicit atomic_ref(T&);
    atomic_ref(const atomic_ref&) noexcept;
    T operator=(T) const noexcept;
    operator T() const noexcept;
    bool is_lock_free() const noexcept;
    void store(T, memory_order = memory_order_seq_cst) const noexcept;
    T load(memory_order = memory_order_seq_cst) const noexcept;
    T exchange(T, memory_order = memory_order_seq_cst) const noexcept;
    bool compare_exchange_weak(T&, T,
                               memory_order, memory_order) const noexcept;
```

- An atomic\_ref object applies atomic operations (31.1) to the object referenced by \*ptr such that, for the lifetime (??) of the atomic\_ref object, the object referenced by \*ptr is an atomic object (??).
- The program is ill-formed if is\_trivially\_copyable\_v<T> is false. The template argument for T shall be trivially copyable (??).
- <sup>3</sup> The lifetime (??) of an object referenced by \*ptr shall exceed the lifetime of all atomic\_refs that reference the object. While any atomic\_ref instances exist that reference the \*ptr object, all accesses to that object shall exclusively occur through those atomic\_ref instances. No subobject of the object referenced by atomic\_ref shall be concurrently referenced by any other atomic\_ref object.
- <sup>4</sup> Atomic operations applied to an object through a referencing atomic\_ref are atomic with respect to atomic operations applied through any other atomic\_ref referencing the same object. [Note: Atomic operations or the atomic\_ref constructor could acquire a shared resource, such as a lock associated with the referenced object, to enable atomic operations to be applied to the referenced object. end note]

#### 31.7.1 Operations

[atomics.ref.operations]

static constexpr bool is\_always\_lock\_free;

The static data member is\_always\_lock\_free is true if the atomic\_ref type's operations are always lock-free, and false otherwise.

static constexpr size\_t required\_alignment;

- The alignment required for an object to be referenced by an atomic reference, which is at least alignof(T).
- [Note: Hardware could require an object referenced by an atomic\_ref to have stricter alignment (??) than other objects of type T. Further, whether operations on an atomic\_ref are lock-free could depend on the alignment of the referenced object. For example, lock-free operations on std::complex<double> could be supported only if aligned to 2\*alignof(double). end note]

```
atomic_ref(T& obj);
```

- 4 Requires: Expects: The referenced object shall be a aligned to required\_alignment.
- 5 Effects: Constructs an atomic reference that references the object. Ensures: \*this references obj.
- 6 Throws: Nothing.

atomic\_ref(const atomic\_ref& ref) noexcept;

7 Effects: Constructs an atomic reference that references the object referenced by ref. Ensures: \*this references the object referenced by ref.

T operator=(T desired) const noexcept;

\*\*Effects: Equivalent to:

store(desired);
return desired;

operator T() const noexcept;

\*\*Effects: Equivalent to: return load();

bool is\_lock\_free() const noexcept;

\*\*Returns: true if the object's operations are lock-free, false otherwise.

```
void store(T desired, memory_order order = memory_order_seq_cst) const noexcept;
11
         Requires: Expects: The order argument shall not be neither memory_order_consume, memory_-
         order_acquire, nor memory_order_acq_rel.
12
         Effects: Atomically replaces the value referenced by *ptr with the value of desired. Memory is
         affected according to the value of order.
   T load(memory_order order = memory_order_seq_cst) const noexcept;
13
         Requires: Expects: The order argument shall not be neither memory_order_release nor memory_-
         order_acq_rel.
14
         Effects: Memory is affected according to the value of order.
15
         Returns: Atomically returns the value referenced by *ptr.
   T exchange(T desired, memory_order order = memory_order_seq_cst) const noexcept;
16
         Effects: Atomically replaces the value referenced by *ptr with desired. Memory is affected according
         to the value of order. This operation is an atomic read-modify-write operation (??).
17
         Returns: Atomically returns the value referenced by *ptr immediately before the effects.
   bool compare_exchange_weak(T& expected, T desired,
                               memory_order success, memory_order failure) const noexcept;
   bool compare_exchange_strong(T& expected, T desired,
                                 memory_order success, memory_order failure) const noexcept;
   bool compare_exchange_weak(T& expected, T desired,
                               memory_order order = memory_order_seq_cst) const noexcept;
   bool compare_exchange_strong(T& expected, T desired,
                                 memory_order order = memory_order_seq_cst) const noexcept;
18
         Requires: Expects: The failure argument shall not be neither memory_order_release nor memory_-
         order_acq_rel.
19
         Effects: Retrieves the value in expected. It then atomically compares the value representation of the
        value referenced by *ptr for equality with that previously retrieved from expected, and if true, replaces
         the value referenced by *ptr with that in desired. If and only if the comparison is true, memory
        is affected according to the value of success, and if the comparison is false, memory is affected
         according to the value of failure. When only one memory_order argument is supplied, the value of
         success is order, and the value of failure is order except that a value of memory_order_acq_rel
        shall be replaced by the value memory_order_acquire and a value of memory_order_release shall
        be replaced by the value memory_order_relaxed. If and only if the comparison is false then, after
         the atomic operation, the value in expected is replaced by the value read from the value referenced
        by *ptr during the atomic comparison. If the operation returns true, these operations are atomic
         read-modify-write operations (??) on the value referenced by *ptr. Otherwise, these operations are
         atomic load operations on that memory.
20
         Returns: The result of the comparison.
21
         Remarks: A weak compare-and-exchange operation may fail spuriously. That is, even when the
         contents of memory referred to by expected and ptr are equal, it may return false and store back
         to expected the same memory contents that were originally there. [Note: This spurious failure
         enables implementation of compare-and-exchange on a broader class of machines, e.g., load-locked
        store-conditional machines. A consequence of spurious failure is that nearly all uses of weak compare-
        and-exchange will be in a loop. When a compare-and-exchange is in a loop, the weak version will yield
        better performance on some platforms. When a weak compare-and-exchange would require a loop and
         a strong one would not, the strong one is preferable. -end \ note
   void wait(T old, memory_order order = memory_order::seq_cst) const noexcept;
22
         Expects: order is neither memory_order::release nor memory_order::acq_rel.
23
         Effects: Repeatedly performs the following steps, in order:
```

§ 31.7.1

— Evaluates load(order) and compares its value representation for equality against that of old.

(23.1)

```
(23.2) — If they compare unequal, returns.
```

(23.3) — Blocks until it is unblocked by an atomic notifying operation or is unblocked spuriously.

24 Remarks: This function is an atomic waiting operation (31.6) on atomic object \*ptr.

```
void notify_one() noexcept;
```

Expects: Unblocks the execution of at least one atomic waiting operation on \*ptr that is eligible to be unblocked (31.6) by this call, if any such atomic waiting operations exist.

26 Remarks: This function is an atomic notifying operation (31.6) on atomic object \*ptr.

```
void notify_all() noexcept;
```

- Expects: Unblocks the execution of all atomic waiting operations on \*ptr that are eligible to be unblocked (31.6) by this call.
- 28 Remarks: This function is an atomic notifying operation (31.6) on atomic object \*ptr.

#### 31.7.2 Specializations for integral types

#### [atomics.ref.int]

There are specializations of the atomic\_ref class template for the integral types char, signed char, unsigned char, short, unsigned short, int, unsigned int, long, unsigned long, long long, unsigned long long, char8\_t, char16\_t, char32\_t, wchar\_t, and any other types needed by the typedefs in the header <cstdint>. For each such type integral, the specialization atomic\_ref<integral> provides additional atomic operations appropriate to integral types. [Note: For the specialization atomic\_ref<bool>, see 31.7. — end note]

```
namespace std {
  template<> struct atomic_ref<integral> {
    integral* ptr;
                           // exposition only
  public:
    using value_type = integral;
    using difference_type = value_type;
    static constexpr bool is_always_lock_free = implementation-defined;
    static constexpr size_t required_alignment = implementation-defined;
    atomic_ref& operator=(const atomic_ref&) = delete;
    explicit atomic_ref(integral&);
    atomic_ref(const atomic_ref&) noexcept;
    integral operator=(integral) const noexcept;
    operator integral() const noexcept;
    bool is_lock_free() const noexcept;
    void store(integral, memory_order = memory_order_seq_cst) const noexcept;
    integral load(memory_order = memory_order_seq_cst) const noexcept;
    integral exchange(integral,
                      memory_order = memory_order_seq_cst) const noexcept;
    bool compare_exchange_weak(integral&, integral,
                               memory_order, memory_order) const noexcept;
    bool compare_exchange_strong(integral &, integral ,
                                 memory_order, memory_order) const noexcept;
    bool compare_exchange_weak(integral&, integral,
                               memory_order = memory_order_seq_cst) const noexcept;
    bool compare_exchange_strong(integral &, integral ,
                                 memory_order = memory_order_seq_cst) const noexcept;
    integral fetch_add(integral,
                       memory_order = memory_order_seq_cst) const noexcept;
    integral fetch_sub(integral,
                       memory_order = memory_order_seq_cst) const noexcept;
    integral fetch_and(integral,
                       memory_order = memory_order_seq_cst) const noexcept;
```

```
integral fetch_or(integral,
                    memory_order = memory_order_seq_cst) const noexcept;
  integral fetch_xor(integral,
                     memory_order = memory_order_seq_cst) const noexcept;
  void wait(integral, memory_order = memory_order::seq_cst) const noexcept;
  void notify_one() noexcept;
  void notify_all() noexcept;
  integral operator++(int) const noexcept;
  integral operator--(int) const noexcept;
  integral operator++() const noexcept;
  integral operator--() const noexcept;
  integral operator+=(integral) const noexcept;
  integral operator-=(integral) const noexcept;
  integral operator&=(integral) const noexcept;
  integral operator|=(integral) const noexcept;
  integral operator^=(integral) const noexcept;
};
```

- 2 Descriptions are provided below only for members that differ from the primary template.
- The following operations perform arithmetic computations. The key, operator, and computation correspondence is identified in Table 143.

```
integral fetch_key(integral operand, memory_order order = memory_order_seq_cst) const noexcept;
```

- 4 Effects: Atomically replaces the value referenced by \*ptr with the result of the computation applied to the value referenced by \*ptr and the given operand. Memory is affected according to the value of order. These operations are atomic read-modify-write operations (??).
- 5 Returns: Atomically, the value referenced by \*ptr immediately before the effects.
- Remarks: For signed integer types, the result is as if the object value and parameters were converted to their corresponding unsigned types, the computation performed on those types, and the result converted back to the signed type. [Note: There are no undefined results arising from the computation. end note]

integral operator op=(integral operand) const noexcept;

7 Effects: Equivalent to: return fetch\_key(operand) op operand;

#### 31.7.3 Specializations for floating-point types

[atomics.ref.float]

<sup>1</sup> There are specializations of the atomic\_ref class template for the floating-point types float, double, and long double. For each such type floating-point, the specialization atomic\_ref<floating-point> provides additional atomic operations appropriate to floating-point types.

```
namespace std {
  template<> struct atomic_ref<floating-point> {
    private:
      floating-point* ptr; // exposition only
    public:
      using value_type = floating-point;
      using difference_type = value_type;
      static constexpr bool is_always_lock_free = implementation-defined;
      static constexpr size_t required_alignment = implementation-defined;
      atomic_ref& operator=(const atomic_ref&) = delete;
      explicit atomic_ref(floating-point&);
      atomic_ref(const atomic_ref&) noexcept;
      floating-point operator=(floating-point) noexcept;
      operator floating-point() const noexcept;
```

```
bool is_lock_free() const noexcept;
  void store(floating-point, memory_order = memory_order_seq_cst) const noexcept;
  floating-point load(memory_order = memory_order_seq_cst) const noexcept;
  floating-point exchange(floating-point,
                          memory_order = memory_order_seq_cst) const noexcept;
  bool compare_exchange_weak(floating-point&, floating-point,
                             memory_order, memory_order) const noexcept;
  \verb|boolcompare_exchange_strong| (floating-point \&, floating-point,
                               memory_order, memory_order) const noexcept;
  bool compare_exchange_weak(floating-point&, floating-point,
                             memory_order = memory_order_seq_cst) const noexcept;
  bool compare_exchange_strong(floating-point&, floating-point,
                               memory_order = memory_order_seq_cst) const noexcept;
  floating-point fetch_add(floating-point,
                           memory_order = memory_order_seq_cst) const noexcept;
  floating-point fetch_sub(floating-point,
                           memory_order = memory_order_seq_cst) const noexcept;
  void wait(floating-point, memory_order = memory_order::seq_cst) const noexcept;
  void notify_one() noexcept;
  void notify_all() noexcept;
  floating-point operator+=(floating-point) const noexcept;
  floating-point operator-=(floating-point) const noexcept;
};
```

- <sup>2</sup> Descriptions are provided below only for members that differ from the primary template.
- The following operations perform arithmetic computations. The key, operator, and computation correspondence are identified in Table 143.

- Effects: Atomically replaces the value referenced by \*ptr with the result of the computation applied to the value referenced by \*ptr and the given operand. Memory is affected according to the value of order. These operations are atomic read-modify-write operations (??).
- 5 Returns: Atomically, the value referenced by \*ptr immediately before the effects.
- Remarks: If the result is not a representable value for its type (??), the result is unspecified, but the operations otherwise have no undefined behavior. Atomic arithmetic operations on floating-point should conform to the std::numeric\_limits<floating-point> traits associated with the floating-point type (??). The floating-point environment (??) for atomic arithmetic operations on floating-point may be different than the calling thread's floating-point environment.

```
floating-point operator op=(floating-point operand) const noexcept;
Effects: Equivalent to: return fetch_key (operand) op operand;
```

### 31.7.4 Partial specialization for pointers

[atomics.ref.pointer]

```
explicit atomic_ref(T*&);
        atomic_ref(const atomic_ref&) noexcept;
        T* operator=(T*) const noexcept;
        operator T*() const noexcept;
        bool is_lock_free() const noexcept;
        void store(T*, memory_order = memory_order_seq_cst) const noexcept;
        T* load(memory_order = memory_order_seq_cst) const noexcept;
        T* exchange(T*, memory_order = memory_order_seq_cst) const noexcept;
        bool compare_exchange_weak(T*&, T*,
                                    memory_order, memory_order) const noexcept;
        bool compare_exchange_strong(T*&, T*,
                                      memory_order, memory_order) const noexcept;
        bool compare_exchange_weak(T*&, T*,
                                    memory_order = memory_order_seq_cst) const noexcept;
        bool compare_exchange_strong(T*&, T*,
                                      memory_order = memory_order_seq_cst) const noexcept;
        T* fetch_add(difference_type, memory_order = memory_order_seq_cst) const noexcept;
        T* fetch_sub(difference_type, memory_order = memory_order_seq_cst) const noexcept;
        void wait(T*, memory_order = memory_order::seq_cst) const noexcept;
        void notify_one() noexcept;
        void notify_all() noexcept;
        T* operator++(int) const noexcept;
        T* operator--(int) const noexcept;
        T* operator++() const noexcept;
        T* operator--() const noexcept;
        T* operator+=(difference_type) const noexcept;
        T* operator==(difference_type) const noexcept;
<sup>1</sup> Descriptions are provided below only for members that differ from the primary template.
  The following operations perform arithmetic computations. The key, operator, and computation correspon-
  dence is identified in Table 144.
  T* fetch_key(difference_type operand, memory_order order = memory_order_seq_cst) const noexcept;
        Requires: Mandates: T shall be an is a complete object type, otherwise the program is ill-formed.
        Effects: Atomically replaces the value referenced by *ptr with the result of the computation applied
        to the value referenced by *ptr and the given operand. Memory is affected according to the value of
        order. These operations are atomic read-modify-write operations (??).
        Returns: Atomically, the value referenced by *ptr immediately before the effects.
        Remarks: The result may be an undefined address, but the operations otherwise have no undefined
        behavior.
  T* operator op = (difference_type operand) const noexcept;
        Effects: Equivalent to: return fetch_key (operand) op operand;
            Member operators common to integers and pointers to objects
  31.7.5
            [atomics.ref.memop]
  T* operator++(int) const noexcept;
```

3

4

5

6

1

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Effects: Equivalent to: return fetch\_add(1);

Effects: Equivalent to: return fetch\_sub(1);

T\* operator--(int) const noexcept;

```
T* operator++() const noexcept;
          Effects: Equivalent to: return fetch_add(1) + 1;
     T* operator--(int) const noexcept;
          Effects: Equivalent to: return fetch sub(1) - 1;
            Class template atomic
                                                                               [atomics.types.generic]
       namespace std {
         template<class T> struct atomic {
           using value_type = T;
           static constexpr bool is_always_lock_free = implementation-defined;
           bool is_lock_free() const volatile noexcept;
           bool is_lock_free() const noexcept;
           void store(T, memory_order = memory_order::seq_cst) volatile noexcept;
           void store(T, memory_order = memory_order::seq_cst) noexcept;
           T load(memory_order = memory_order::seq_cst) const volatile noexcept;
           T load(memory_order = memory_order::seq_cst) const noexcept;
           operator T() const volatile noexcept;
           operator T() const noexcept;
           T exchange(T, memory_order = memory_order::seq_cst) volatile noexcept;
           T exchange(T, memory_order = memory_order::seq_cst) noexcept;
           bool compare_exchange_weak(T&, T, memory_order, memory_order) volatile noexcept;
           bool compare_exchange_weak(T&, T, memory_order, memory_order) noexcept;
           bool compare_exchange_strong(T&, T, memory_order, memory_order) volatile noexcept;
           bool compare_exchange_strong(T&, T, memory_order, memory_order) noexcept;
           bool compare_exchange_weak(T&, T, memory_order = memory_order::seq_cst) volatile noexcept;
           bool compare_exchange_weak(T&, T, memory_order = memory_order::seq_cst) noexcept;
           bool compare_exchange_strong(T&, T, memory_order = memory_order::seq_cst) volatile noexcept;
           bool compare_exchange_strong(T&, T, memory_order = memory_order::seq_cst) noexcept;
           void wait(T, memory_order = memory_order::seq_cst) const volatile noexcept;
           void wait(T, memory_order = memory_order::seq_cst) const noexcept;
           void notify_one() volatile noexcept;
           void notify_one() noexcept;
           void notify_all() volatile noexcept;
           void notify_all() noexcept;
           atomic() noexcept = default;
           constexpr atomic(T) noexcept;
           atomic(const atomic&) = delete;
           atomic& operator=(const atomic&) = delete;
           atomic& operator=(const atomic&) volatile = delete;
           T operator=(T) volatile noexcept;
           T operator=(T) noexcept;
  <sup>1</sup> The template argument for T shall meet the Cpp17CopyConstructible and Cpp17CopyAssignable requirements.
     The program is ill-formed if any of
(1.1)
       — is_trivially_copyable_v<T>,
(1.2)
       — is_copy_constructible_v<T>,
(1.3)
       — is_move_constructible_v<T>,
(1.4)
       — is_copy_assignable_v<T>, or
(1.5)
       — is_move_assignable_v<T>
     is false. [Note: Type arguments that are not also statically initializable may be difficult to use. — end
     note
  <sup>2</sup> The specialization atomic<bool> is a standard-layout struct.
```

§ 31.8

[Note: The representation of an atomic specialization need not have the same size and alignment requirement

as its corresponding argument type. -end note

#### 31.8.1 Operations on atomic types

#### [atomics.types.operations]

<sup>1</sup> [Note: Many operations are volatile-qualified. The "volatile as device register" semantics have not changed in the standard. This qualification means that volatility is preserved when applying these operations to volatile objects. It does not mean that operations on non-volatile objects become volatile. — end note]

```
atomic() noexcept = default;
```

Effects: Leaves the atomic object in an uninitialized state. [Note: These semantics ensure compatibility with C.  $-end\ note$ ]

```
constexpr atomic(T desired) noexcept;
```

Effects: Initializes the object with the value desired. Initialization is not an atomic operation (??). [Note: It is possible to have an access to an atomic object A race with its construction, for example by communicating the address of the just-constructed object A to another thread via memory\_order::relaxed operations on a suitable atomic pointer variable, and then immediately accessing A in the receiving thread. This results in undefined behavior. —end note]

```
#define ATOMIC_VAR_INIT(value) see below
```

The macro expands to a token sequence suitable for constant initialization of an atomic variable of static storage duration of a type that is initialization-compatible with value. [Note: This operation may need to initialize locks. — end note] Concurrent access to the variable being initialized, even via an atomic operation, constitutes a data race. [Example:

The static data member is\_always\_lock\_free is true if the atomic type's operations are always lock-free, and false otherwise. [Note: The value of is\_always\_lock\_free is consistent with the value of the corresponding ATOMIC\_...\_LOCK\_FREE macro, if defined. — end note]

```
bool is_lock_free() const volatile noexcept;
bool is_lock_free() const noexcept;
```

T operator=(T desired) volatile noexcept;

T operator=(T desired) noexcept;

Returns: true if the object's operations are lock-free, false otherwise. [Note: The return value of the is\_lock\_free member function is consistent with the value of is\_always\_lock\_free for the same type. — end note]

```
void store(T desired, memory_order order = memory_order::seq_cst) volatile noexcept;
void store(T desired, memory_order order = memory_order::seq_cst) noexcept;
```

- 7 Requires: Expects: The order argument shall not be neither memory\_order::consume, memory\_order::acquire, nor memory\_order::acq\_rel.
- 8 Effects: Atomically replaces the value pointed to by this with the value of desired. Memory is affected according to the value of order.

13 Returns: Atomically returns the value pointed to by this.

14

```
T exchange(T desired, memory_order order = memory_order::seq_cst) volatile noexcept;
   T exchange(T desired, memory_order order = memory_order::seq_cst) noexcept;
15
         Effects: Atomically replaces the value pointed to by this with desired. Memory is affected according
         to the value of order. These operations are atomic read-modify-write operations (??).
16
         Returns: Atomically returns the value pointed to by this immediately before the effects.
   bool compare_exchange_weak(T& expected, T desired,
                               memory_order success, memory_order failure) volatile noexcept;
   bool compare_exchange_weak(T& expected, T desired,
                               memory_order success, memory_order failure) noexcept;
   bool compare_exchange_strong(T& expected, T desired,
                                 memory_order success, memory_order failure) volatile noexcept;
   bool compare_exchange_strong(T& expected, T desired,
                                 memory_order success, memory_order failure) noexcept;
   bool compare_exchange_weak(T& expected, T desired,
                               memory_order order = memory_order::seq_cst) volatile noexcept;
   bool compare_exchange_weak(T& expected, T desired,
                               memory_order order = memory_order::seq_cst) noexcept;
   bool compare_exchange_strong(T& expected, T desired,
                                 memory_order order = memory_order::seq_cst) volatile noexcept;
   bool compare_exchange_strong(T& expected, T desired,
                                 memory_order order = memory_order::seq_cst) noexcept;
17
         Requires: Expects: The failure argument shall not be is neither memory_order::release nor memory_-
         order::acq_rel.
18
         Effects: Retrieves the value in expected. It then atomically compares the value representation of
         the value pointed to by this for equality with that previously retrieved from expected, and if true,
         replaces the value pointed to by this with that in desired. If and only if the comparison is true,
         memory is affected according to the value of success, and if the comparison is false, memory is affected
         according to the value of failure. When only one memory_order argument is supplied, the value of
         success is order, and the value of failure is order except that a value of memory_order::acq_rel
        shall be replaced by the value memory_order::acquire and a value of memory_order::release shall
        be replaced by the value memory_order::relaxed. If and only if the comparison is false then, after
         the atomic operation, the value in expected is replaced by the value pointed to by this during the
         atomic comparison. If the operation returns true, these operations are atomic read-modify-write
         operations (??) on the memory pointed to by this. Otherwise, these operations are atomic load
         operations on that memory.
19
         Returns: The result of the comparison.
20
         [Note: For example, the effect of compare_exchange_strong on objects without padding bits (??) is
           if (memcmp(this, &expected, sizeof(*this)) == 0)
             memcpy(this, &desired, sizeof(*this));
           else
             memcpy(expected, this, sizeof(*this));
          — end note] [Example: The expected use of the compare-and-exchange operations is as follows. The
        compare-and-exchange operations will update expected when another iteration of the loop is needed.
           expected = current.load();
           do {
             desired = function(expected);
           } while (!current.compare_exchange_weak(expected, desired));
         — end example [Example: Because the expected value is updated only on failure, code releasing the
         memory containing the expected value on success will work. For example, list head insertion will act
        atomically and would not introduce a data race in the following code:
           do {
             p->next = head; // make new list node point to the current head
           } while (!head.compare_exchange_weak(p->next, p)); // try \ to \ insert
         — end example]
```

Implementations should ensure that weak compare-and-exchange operations do not consistently return false unless either the atomic object has value different from expected or there are concurrent modifications to the atomic object.

- Remarks: A weak compare-and-exchange operation may fail spuriously. That is, even when the contents of memory referred to by expected and this are equal, it may return false and store back to expected the same memory contents that were originally there. [Note: This spurious failure enables implementation of compare-and-exchange on a broader class of machines, e.g., load-locked store-conditional machines. A consequence of spurious failure is that nearly all uses of weak compare-and-exchange will be in a loop. When a compare-and-exchange is in a loop, the weak version will yield better performance on some platforms. When a weak compare-and-exchange would require a loop and a strong one would not, the strong one is preferable. end note]
- [Note: Under cases where the memcpy and memcmp semantics of the compare-and-exchange operations apply, the outcome might be failed comparisons for values that compare equal with operator== if the value representation has trap bits or alternate representations of the same value. Notably, on implementations conforming to ISO/IEC/IEEE 60559, floating-point -0.0 and +0.0 will not compare equal with memcmp but will compare equal with operator==, and NaNs with the same payload will compare equal with memcmp but will not compare equal with operator==. end note] [Note: Because compare-and-exchange acts on an object's value representation, padding bits that never participate in the object's value representation are ignored. As a consequence, the following code is guaranteed to avoid spurious failure:

```
struct padded {
  char clank = 0x42;
  // Padding here.
  unsigned biff = 0xCODEFEFE;
};
atomic<padded> pad = ATOMIC_VAR_INIT({});

bool zap() {
  padded expected, desired{0, 0};
  return pad.compare_exchange_strong(expected, desired);
}
```

24

25

(25.1)

(25.2)

(25.3)

26

— end note] [Note: For a union with bits that participate in the value representation of some members but not others, compare-and-exchange might always fail. This is because such padding bits have an indeterminate value when they do not participate in the value representation of the active member. As a consequence, the following code is not guaranteed to ever succeed:

```
union pony {
         double celestia = 0.;
         short luna; // padded
       };
       atomic<pony> princesses = ATOMIC_VAR_INIT({});
       bool party(pony desired) {
         pony expected;
         return princesses.compare_exchange_strong(expected, desired);
      - end note]
void wait(T old, memory_order order = memory_order::seq_cst) const volatile noexcept;
void wait(T old, memory_order order = memory_order::seq_cst) const noexcept;
     Expects: order is neither memory_order::release nor memory_order::acq_rel.
     Effects: Repeatedly performs the following steps, in order:

    Evaluates load(order) and compares its value representation for equality against that of old.

       — If they compare unequal, returns.

    Blocks until it is unblocked by an atomic notifying operation or is unblocked spuriously.

     Remarks: This function is an atomic waiting operation (31.6).
```

```
void notify_one() volatile noexcept;
void notify_one() noexcept;

Effects: Unblocks the execution of at least one atomic waiting operation that is eligible to be unblocked
(31.6) by this call, if any such atomic waiting operations exist.

Remarks: This function is an atomic notifying operation (31.6).
```

```
void notify_all() volatile noexcept;
void notify_all() noexcept;
```

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29 Effects: Unblocks the execution of all atomic waiting operations that are eligible to be unblocked (31.6) by this call.

Remarks: This function is an atomic notifying operation (31.6).

#### 31.8.2 Specializations for integers

#### [atomics.types.int]

There are specializations of the atomic class template for the integral types char, signed char, unsigned char, short, unsigned short, int, unsigned int, long, unsigned long, long long, unsigned long, char8\_t, char16\_t, char32\_t, wchar\_t, and any other types needed by the typedefs in the header <cstdint>. For each such type integral, the specialization atomic<integral> provides additional atomic operations appropriate to integral types. [Note: For the specialization atomic<br/>bool>, see 31.8. — end note]

```
namespace std {
  template<> struct atomic<integral> {
    using value_type = integral;
    using difference_type = value_type;
    static constexpr bool is_always_lock_free = implementation-defined;
    bool is_lock_free() const volatile noexcept;
    bool is_lock_free() const noexcept;
    void store(integral, memory_order = memory_order::seq_cst) volatile noexcept;
    void store(integral, memory_order = memory_order::seq_cst) noexcept;
    integral load(memory_order = memory_order::seq_cst) const volatile noexcept;
    integral load(memory_order = memory_order::seq_cst) const noexcept;
    operator integral() const volatile noexcept;
    operator integral() const noexcept;
    integral exchange(integral, memory_order = memory_order::seq_cst) volatile noexcept;
    integral exchange(integral, memory_order = memory_order::seq_cst) noexcept;
    bool compare_exchange_weak(integral&, integral,
                                memory_order, memory_order) volatile noexcept;
    bool compare_exchange_weak(integral&, integral,
                                memory_order, memory_order) noexcept;
    bool compare_exchange_strong(integral&, integral,
                                  memory_order, memory_order) volatile noexcept;
    \verb|boolcompare_exchange_strong| (integral \&, integral,
                                  memory_order, memory_order) noexcept;
    bool compare_exchange_weak(integral&, integral,
                                memory_order = memory_order::seq_cst) volatile noexcept;
    bool compare_exchange_weak(integral&, integral,
                                memory_order = memory_order::seq_cst) noexcept;
    bool compare_exchange_strong(integral&, integral,
                                  memory_order = memory_order::seq_cst) volatile noexcept;
    bool compare_exchange_strong(integral&, integral,
                                  memory_order = memory_order::seq_cst) noexcept;
    integral fetch_add(integral, memory_order = memory_order::seq_cst) volatile noexcept;
    integral fetch_add(integral, memory_order = memory_order::seq_cst) noexcept;
    integral fetch_sub(integral, memory_order = memory_order::seq_cst) volatile noexcept;
    integral fetch_sub(integral, memory_order = memory_order::seq_cst) noexcept;
    integral fetch_and(integral, memory_order = memory_order::seq_cst) volatile noexcept;
    integral fetch_and(integral, memory_order = memory_order::seq_cst) noexcept;
    integral fetch_or(integral, memory_order = memory_order::seq_cst) volatile noexcept;
    integral fetch_or(integral, memory_order = memory_order::seq_cst) noexcept;
    integral fetch_xor(integral, memory_order = memory_order::seq_cst) volatile noexcept;
    integral \  \, \texttt{fetch\_xor}(integral, \  \, \texttt{memory\_order} = \  \, \texttt{memory\_order}:: \texttt{seq\_cst}) \  \, \texttt{noexcept};
```

```
void wait(integral, memory_order = memory_order::seq_cst) const volatile noexcept;
    void wait(integral, memory_order = memory_order::seq_cst) const noexcept;
    void notify_one() volatile noexcept;
    void notify_one() noexcept;
    void notify_all() volatile noexcept;
    void notify_all() noexcept;
    atomic() noexcept = default;
    constexpr atomic(integral) noexcept;
    atomic(const atomic&) = delete;
    atomic& operator=(const atomic&) = delete;
    atomic& operator=(const atomic&) volatile = delete;
    integral operator=(integral) volatile noexcept;
    integral operator=(integral) noexcept;
    integral operator++(int) volatile noexcept;
    integral operator++(int) noexcept;
    integral operator--(int) volatile noexcept;
    integral operator--(int) noexcept;
    integral operator++() volatile noexcept;
    integral operator++() noexcept;
    integral operator--() volatile noexcept;
    integral operator--() noexcept;
    integral operator+=(integral) volatile noexcept;
    integral operator+=(integral) noexcept;
    integral operator==(integral) volatile noexcept;
    integral operator-=(integral) noexcept;
    integral operator&=(integral) volatile noexcept;
    integral operator&=(integral) noexcept;
    integral operator|=(integral) volatile noexcept;
    integral operator|=(integral) noexcept;
    integral operator^=(integral) volatile noexcept;
    integral operator^=(integral) noexcept;
}
```

- <sup>2</sup> The atomic integral specializations are standard-layout structs. They each have a trivial default constructor and a trivial destructor.
- 3 Descriptions are provided below only for members that differ from the primary template.
- <sup>4</sup> The following operations perform arithmetic computations. The key, operator, and computation correspondence is:

Table 143: Atomic arithmetic computations [tab:atomic.types.int.comp]

key	Op	Computation	key	Op	Computation
add	+	addition	sub	_	subtraction
or	1	bitwise inclusive or	xor	^	bitwise exclusive or
and	&	bitwise and			

```
T fetch_key(T operand, memory_order order = memory_order::seq_cst) volatile noexcept;
T fetch_key(T operand, memory_order order = memory_order::seq_cst) noexcept;
```

- Effects: Atomically replaces the value pointed to by this with the result of the computation applied to the value pointed to by this and the given operand. Memory is affected according to the value of order. These operations are atomic read-modify-write operations (??).
- 6 Returns: Atomically, the value pointed to by this immediately before the effects.
- Remarks: For signed integer types, the result is as if the object value and parameters were converted to their corresponding unsigned types, the computation performed on those types, and the result converted back to the signed type. [Note: There are no undefined results arising from the computation. end note]

```
T operator op = (T operand) volatile noexcept;
T operator op = (T operand) noexcept;

Effects: Equivalent to: return fetch_key (operand) op operand;
```

#### 31.8.3 Specializations for floating-point types

[atomics.types.float]

<sup>1</sup> There are specializations of the atomic class template for the floating-point types float, double, and long double. For each such type floating-point, the specialization atomic<floating-point> provides additional atomic operations appropriate to floating-point types.

```
namespace std {
  template<> struct atomic<floating-point> {
    using value_type = floating-point;
    using difference_type = value_type;
    static constexpr bool is_always_lock_free = implementation-defined;
    bool is_lock_free() const volatile noexcept;
    bool is_lock_free() const noexcept;
    void store(floating-point, memory_order = memory_order_seq_cst) volatile noexcept;
    void store(floating-point, memory_order = memory_order_seq_cst) noexcept;
    floating-point load(memory_order = memory_order_seq_cst) volatile noexcept;
    floating-point load(memory_order = memory_order_seq_cst) noexcept;
    operator floating-point() volatile noexcept;
    operator floating-point() noexcept;
    floating-point exchange(floating-point,
                            memory_order = memory_order_seq_cst) volatile noexcept;
    floating-point exchange(floating-point,
                            memory_order = memory_order_seq_cst) noexcept;
    \verb|boolcompare_exchange_weak| (floating-point \&, floating-point,
                               memory_order, memory_order) volatile noexcept;
    bool compare_exchange_weak(floating-point&, floating-point,
                               memory_order, memory_order) noexcept;
    \verb|boolcompare_exchange_strong| (floating-point \&, floating-point,
                                 memory_order, memory_order) volatile noexcept;
    bool compare_exchange_strong(floating-point&, floating-point,
                                 memory_order, memory_order) noexcept;
    bool compare_exchange_weak(floating-point&, floating-point,
                               memory_order = memory_order_seq_cst) volatile noexcept;
    bool compare_exchange_weak(floating-point&, floating-point,
                               memory_order = memory_order_seq_cst) noexcept;
    bool compare_exchange_strong(floating-point&, floating-point,
                                 memory_order = memory_order_seq_cst) volatile noexcept;
    bool compare_exchange_strong(floating-point&, floating-point,
                                 memory_order = memory_order_seq_cst) noexcept;
    floating-point fetch_add(floating-point,
                             memory_order = memory_order_seq_cst) volatile noexcept;
    floating-point fetch_add(floating-point,
                             memory_order = memory_order_seq_cst) noexcept;
    floating-point fetch_sub(floating-point,
                             memory_order = memory_order_seq_cst) volatile noexcept;
    floating-point fetch_sub(floating-point,
                             memory_order = memory_order_seq_cst) noexcept;
    void wait(floating-point, memory_order = memory_order::seq_cst) const volatile noexcept;
    void wait(floating-point, memory_order = memory_order::seq_cst) const noexcept;
    void notify_one() volatile noexcept;
    void notify_one() noexcept;
    void notify_all() volatile noexcept;
    void notify_all() noexcept;
    atomic() noexcept = default;
    constexpr atomic(floating-point) noexcept;
    atomic(const atomic&) = delete;
    atomic& operator=(const atomic&) = delete;
```

```
atomic& operator=(const atomic&) volatile = delete;
floating-point operator=(floating-point) volatile noexcept;
floating-point operator=(floating-point) noexcept;

floating-point operator+=(floating-point) volatile noexcept;
floating-point operator+=(floating-point) noexcept;
floating-point operator-=(floating-point) volatile noexcept;
floating-point operator-=(floating-point) noexcept;
floating-point operator-=(floating-point) noexcept;
};
}
```

- <sup>2</sup> The atomic floating-point specializations are standard-layout structs. They each have a trivial default constructor and a trivial destructor.
- 3 Descriptions are provided below only for members that differ from the primary template.
- <sup>4</sup> The following operations perform arithmetic addition and subtraction computations. The key, operator, and computation correspondence are identified in Table 143.

```
T A::fetch_key(T operand, memory_order order = memory_order_seq_cst) volatile noexcept;
T A::fetch_key(T operand, memory_order order = memory_order_seq_cst) noexcept;
```

- Effects: Atomically replaces the value pointed to by this with the result of the computation applied to the value pointed to by this and the given operand. Memory is affected according to the value of order. These operations are atomic read-modify-write operations (??).
- 6 Returns: Atomically, the value pointed to by this immediately before the effects.
- Remarks: If the result is not a representable value for its type (??) the result is unspecified, but the operations otherwise have no undefined behavior. Atomic arithmetic operations on floating-point should conform to the std::numeric\_limits<floating-point> traits associated with the floating-point type (??). The floating-point environment (??) for atomic arithmetic operations on floating-point may be different than the calling thread's floating-point environment.

```
T operator op = (T operand) volatile noexcept;
T operator op = (T operand) noexcept;

Effects: Equivalent to: return fetch_key (operand) op operand;
```

Remarks: If the result is not a representable value for its type (??) the result is unspecified, but the operations otherwise have no undefined behavior. Atomic arithmetic operations on floating-point should conform to the std::numeric\_limits<floating-point> traits associated with the floating-point type (??). The floating-point environment (??) for atomic arithmetic operations on floating-point may be different than the calling thread's floating-point environment.

#### 31.8.4 Partial specialization for pointers

8

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[atomics.types.pointer]

```
namespace std {
  template<class T> struct atomic<T*> {
    using value_type = T*;
    using difference_type = ptrdiff_t;
    static constexpr bool is_always_lock_free = implementation-defined;
    bool is_lock_free() const volatile noexcept;
    bool is_lock_free() const noexcept;
    void store(T*, memory_order = memory_order::seq_cst) volatile noexcept;
    void store(T*, memory_order = memory_order::seq_cst) noexcept;
    T* load(memory_order = memory_order::seq_cst) const volatile noexcept;
    T* load(memory_order = memory_order::seq_cst) const noexcept;
    operator T*() const volatile noexcept;
    operator T*() const noexcept;
    T* exchange(T*, memory_order = memory_order::seq_cst) volatile noexcept;
    T* exchange(T*, memory_order = memory_order::seq_cst) noexcept;
    bool compare_exchange_weak(T*&, T*, memory_order, memory_order) volatile noexcept;
    \verb|boolcompare_exchange_weak(T*\&, T*, \verb|memory_order|, \verb|memory_order|) | no except; \\
    bool compare_exchange_strong(T*&, T*, memory_order, memory_order) volatile noexcept;
    bool compare_exchange_strong(T*&, T*, memory_order, memory_order) noexcept;
    bool compare_exchange_weak(T*&, T*,
                                memory_order = memory_order::seq_cst) volatile noexcept;
```

```
bool compare_exchange_weak(T*&, T*,
                             memory_order = memory_order::seq_cst) noexcept;
  bool compare_exchange_strong(T*&, T*,
                               memory_order = memory_order::seq_cst) volatile noexcept;
  bool compare_exchange_strong(T*&, T*,
                               memory_order = memory_order::seq_cst) noexcept;
  T* fetch_add(ptrdiff_t, memory_order = memory_order::seq_cst) volatile noexcept;
  T* fetch_add(ptrdiff_t, memory_order = memory_order::seq_cst) noexcept;
  T* fetch_sub(ptrdiff_t, memory_order = memory_order::seq_cst) volatile noexcept;
  T* fetch_sub(ptrdiff_t, memory_order = memory_order::seq_cst) noexcept;
  void wait(T*, memory_order = memory_order::seq_cst) const volatile noexcept;
  void wait(T*, memory_order = memory_order::seq_cst) const noexcept;
  void notify_one() volatile noexcept;
  void notify_one() noexcept;
  void notify_all() volatile noexcept;
  void notify_all() noexcept;
  atomic() noexcept = default;
  constexpr atomic(T*) noexcept;
  atomic(const atomic&) = delete;
  atomic& operator=(const atomic&) = delete;
  atomic& operator=(const atomic&) volatile = delete;
  T* operator=(T*) volatile noexcept;
  T* operator=(T*) noexcept;
  T* operator++(int) volatile noexcept;
  T* operator++(int) noexcept;
  T* operator--(int) volatile noexcept;
  T* operator--(int) noexcept;
  T* operator++() volatile noexcept;
  T* operator++() noexcept;
  T* operator--() volatile noexcept;
  T* operator--() noexcept;
  T* operator+=(ptrdiff_t) volatile noexcept;
  T* operator+=(ptrdiff_t) noexcept;
  T* operator-=(ptrdiff_t) volatile noexcept;
  T* operator-=(ptrdiff_t) noexcept;
};
```

- <sup>1</sup> There is a partial specialization of the **atomic** class template for pointers. Specializations of this partial specialization are standard-layout structs. They each have a trivial default constructor and a trivial destructor.
- <sup>2</sup> Descriptions are provided below only for members that differ from the primary template.
- <sup>3</sup> The following operations perform pointer arithmetic. The key, operator, and computation correspondence is:

Table 144: Atomic pointer computations [tab:atomic.types.pointer.comp]

Key	Op	Computation	Key	Op	Computation
add	+	addition	sub	-	subtraction

```
T* fetch_key(ptrdiff_t operand, memory_order order = memory_order::seq_cst) volatile noexcept;
T* fetch_key(ptrdiff_t operand, memory_order order = memory_order::seq_cst) noexcept;
```

- 4 Requires: Mandates: T shall be an is a complete object type, otherwise the program is ill-formed.

  [Note: Pointer arithmetic on void\* or function pointers is ill-formed. end note]
- Effects: Atomically replaces the value pointed to by this with the result of the computation applied to the value pointed to by this and the given operand. Memory is affected according to the value of order. These operations are atomic read-modify-write operations (??).
- 6 Returns: Atomically, the value pointed to by this immediately before the effects.

7 Remarks: The result may be an undefined address, but the operations otherwise have no undefined behavior.

```
T* operator op = (ptrdiff_t operand) volatile noexcept;
T* operator op = (ptrdiff_t operand) noexcept;

Effects: Equivalent to: return fetch_key (operand) op operand;
```

# 31.8.5 Member operators common to integers and pointers to objects [atomics.types.memop]

#### 31.9 Non-member functions

#### [atomics.nonmembers]

A non-member function template whose name matches the pattern atomic\_f or the pattern atomic\_f\_-explicit invokes the member function f, with the value of the first parameter as the object expression and the values of the remaining parameters (if any) as the arguments of the member function call, in order. An argument for a parameter of type atomic<T>::value\_type\* is dereferenced when passed to the member function call. If no such member function exists, the program is ill-formed.

```
template<class T>
  void atomic_init(volatile atomic<T>* object, typename atomic<T>::value_type desired) noexcept;
template<class T>
  void atomic_init(atomic<T>* object, typename atomic<T>::value_type desired) noexcept;
```

- 2 Expects: This function is applied only to objects that have been default constructed, and then only once.
- Effects: Non-atomically initializes \*object with value desired. This function shall only be applied to objects that have been default constructed, and then only once. [Note: These semantics ensure compatibility with C. —end note] [Note: Concurrent access from another thread, even via an atomic operation, constitutes a data race. —end note]
- <sup>4</sup> [Note: The non-member functions enable programmers to write code that can be compiled as either C or C++, for example in a shared header file. end note]

#### 31.10 Flag type and operations

[atomics.flag]

```
namespace std {
   struct atomic_flag {
     bool test(memory_order = memory_order::seq_cst) const volatile noexcept;
   bool test(memory_order = memory_order::seq_cst) const noexcept;
   bool test_and_set(memory_order = memory_order::seq_cst) volatile noexcept;
   bool test_and_set(memory_order = memory_order::seq_cst) noexcept;
   void clear(memory_order = memory_order::seq_cst) volatile noexcept;
   void clear(memory_order = memory_order::seq_cst) noexcept;
   void wait(bool, memory_order = memory_order::seq_cst) const volatile noexcept;
   void wait(bool, memory_order = memory_order::seq_cst) const noexcept;
   void notify_one() volatile noexcept;
```

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```
void notify_one() noexcept;
    void notify_all() volatile noexcept;
    void notify_all() noexcept;
    atomic_flag() noexcept = default;
    atomic_flag(const atomic_flag&) = delete;
    atomic_flag& operator=(const atomic_flag&) = delete;
    atomic_flag& operator=(const atomic_flag&) volatile = delete;
 };
}
```

- <sup>1</sup> The atomic\_flag type provides the classic test-and-set functionality. It has two states, set and clear.
- <sup>2</sup> Operations on an object of type atomic\_flag shall be lock-free. [Note: Hence the operations should also be address-free. -end note
- The atomic\_flag type is a standard-layout struct. It has a trivial default constructor and a trivial destructor.

```
#define ATOMIC_FLAG_INIT see below
```

4

5

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11

The macro ATOMIC\_FLAG\_INIT shall be is defined in such a way that it can be used to initialize an object of type atomic\_flag to the clear state. The macro can be used in the form:

```
atomic_flag guard = ATOMIC_FLAG_INIT;
```

It is unspecified whether the macro can be used in other initialization contexts. For a complete static-duration object, that initialization shall be static. Unless initialized with ATOMIC FLAG INIT, it is unspecified whether an atomic\_flag object has an initial state of set or clear.

```
bool atomic_flag_test(const volatile atomic_flag* object) noexcept;
bool atomic_flag_test(const atomic_flag* object) noexcept;
bool atomic_flag_test_explicit(const volatile atomic_flag* object,
                               memory_order order) noexcept;
bool atomic_flag_test_explicit(const atomic_flag* object,
                               memory_order order) noexcept;
bool atomic_flag::test(memory_order order = memory_order::seq_cst) const volatile noexcept;
bool atomic_flag::test(memory_order order = memory_order::seq_cst) const noexcept;
     For atomic_flag_test, let order be memory_order::seq_cst.
     Expects: order is neither memory_order::release nor memory_order::acq_rel.
     Effects: Memory is affected according to the value of order.
     Returns: Atomically returns the value pointed to by object or this.
bool atomic_flag_test_and_set(volatile atomic_flag* object) noexcept;
bool atomic_flag_test_and_set(atomic_flag* object) noexcept;
bool atomic_flag_test_and_set_explicit(volatile atomic_flag* object, memory_order order) noexcept;
bool atomic_flag_test_and_set_explicit(atomic_flag* object, memory_order order) noexcept;
bool atomic_flag::test_and_set(memory_order order = memory_order::seq_cst) volatile noexcept;
bool atomic_flag::test_and_set(memory_order order = memory_order::seq_cst) noexcept;
     Effects: Atomically sets the value pointed to by object or by this to true. Memory is affected
     according to the value of order. These operations are atomic read-modify-write operations (??).
```

9

10 Returns: Atomically, the value of the object immediately before the effects.

```
void atomic_flag_clear(volatile atomic_flag* object) noexcept;
void atomic_flag_clear(atomic_flag* object) noexcept;
void atomic_flag_clear_explicit(volatile atomic_flag* object, memory_order order) noexcept;
void atomic_flag_clear_explicit(atomic_flag* object, memory_order order) noexcept;
void atomic_flag::clear(memory_order order = memory_order::seq_cst) volatile noexcept;
void atomic_flag::clear(memory_order order = memory_order::seq_cst) noexcept;
     Requires: Expects: The order argument shall not be neither memory_order::consume, memory_-
     order::acquire, nor memory_order::acq_rel.
```

12 Effects: Atomically sets the value pointed to by object or by this to false. Memory is affected according to the value of order.

```
void atomic_flag_wait(const volatile atomic_flag* object, bool old) noexcept;
      void atomic_flag_wait(const atomic_flag* object, bool old) noexcept;
      void atomic_flag_wait_explicit(const volatile atomic_flag* object,
                                      bool old, memory_order order) noexcept;
      void atomic_flag_wait_explicit(const atomic_flag* object,
                                      bool old, memory_order order) noexcept;
      void atomic_flag::wait(bool old, memory_order order =
                                          memory_order::seq_cst) const volatile noexcept;
      void atomic_flag::wait(bool old, memory_order order =
                                          memory_order::seq_cst) const noexcept;
  13
           For atomic_flag_wait, let order be memory_order::seq_cst. Let flag be object for the non-
           member functions and this for the member functions.
  14
            Expects: order is neither memory_order::release nor memory_order::acq_rel.
  15
            Effects: Repeatedly performs the following steps, in order:
(15.1)
             — Evaluates flag->test(order) != old.
(15.2)

    If the result of that evaluation is true, returns.

(15.3)

    Blocks until it is unblocked by an atomic notifying operation or is unblocked spuriously.

  16
            Remarks: This function is an atomic waiting operation (31.6).
      void atomic_flag_notify_one(volatile atomic_flag* object) noexcept;
      void atomic_flag_notify_one(atomic_flag* object) noexcept;
      void atomic_flag::notify_one() volatile noexcept;
      void atomic_flag::notify_one() noexcept;
  17
            Effects: Unblocks the execution of at least one atomic waiting operation that is eligible to be unblocked
           (31.6) by this call, if any such atomic waiting operations exist.
  18
            Remarks: This function is an atomic notifying operation (31.6).
      void atomic_flag_notify_all(volatile atomic_flag* object) noexcept;
      void atomic_flag_notify_all(atomic_flag* object) noexcept;
      void atomic_flag::notify_all() volatile noexcept;
      void atomic_flag::notify_all() noexcept;
  19
            Effects: Unblocks the execution of all atomic waiting operations that are eligible to be unblocked (31.6)
           by this call.
  20
            Remarks: This function is an atomic notifying operation (31.6).
```

31.11 Fences [atomics.fences]

<sup>1</sup> This subclause introduces synchronization primitives called *fences*. Fences can have acquire semantics, release semantics, or both. A fence with acquire semantics is called an *acquire fence*. A fence with release semantics is called a *release fence*.

- <sup>2</sup> A release fence A synchronizes with an acquire fence B if there exist atomic operations X and Y, both operating on some atomic object M, such that A is sequenced before X, X modifies M, Y is sequenced before B, and Y reads the value written by X or a value written by any side effect in the hypothetical release sequence X would head if it were a release operation.
- <sup>3</sup> A release fence A synchronizes with an atomic operation B that performs an acquire operation on an atomic object M if there exists an atomic operation X such that A is sequenced before X, X modifies M, and B reads the value written by X or a value written by any side effect in the hypothetical release sequence X would head if it were a release operation.
- An atomic operation A that is a release operation on an atomic object M synchronizes with an acquire fence B if there exists some atomic operation X on M such that X is sequenced before B and reads the value written by A or a value written by any side effect in the release sequence headed by A.

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- (5.3) is a release fence, if order == memory\_order::release;
- is both an acquire fence and a release fence, if order == memory\_order::acq\_rel;
- (5.5) is a sequentially consistent acquire and release fence, if order == memory\_order::seq\_cst.

extern "C" void atomic\_signal\_fence(memory\_order order) noexcept;

- Effects: Equivalent to atomic\_thread\_fence(order), except that the resulting ordering constraints are established only between a thread and a signal handler executed in the same thread.
- [Note: atomic\_signal\_fence can be used to specify the order in which actions performed by the thread become visible to the signal handler. Compiler optimizations and reorderings of loads and stores are inhibited in the same way as with atomic\_thread\_fence, but the hardware fence instructions that atomic\_thread\_fence would have inserted are not emitted. end note]

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